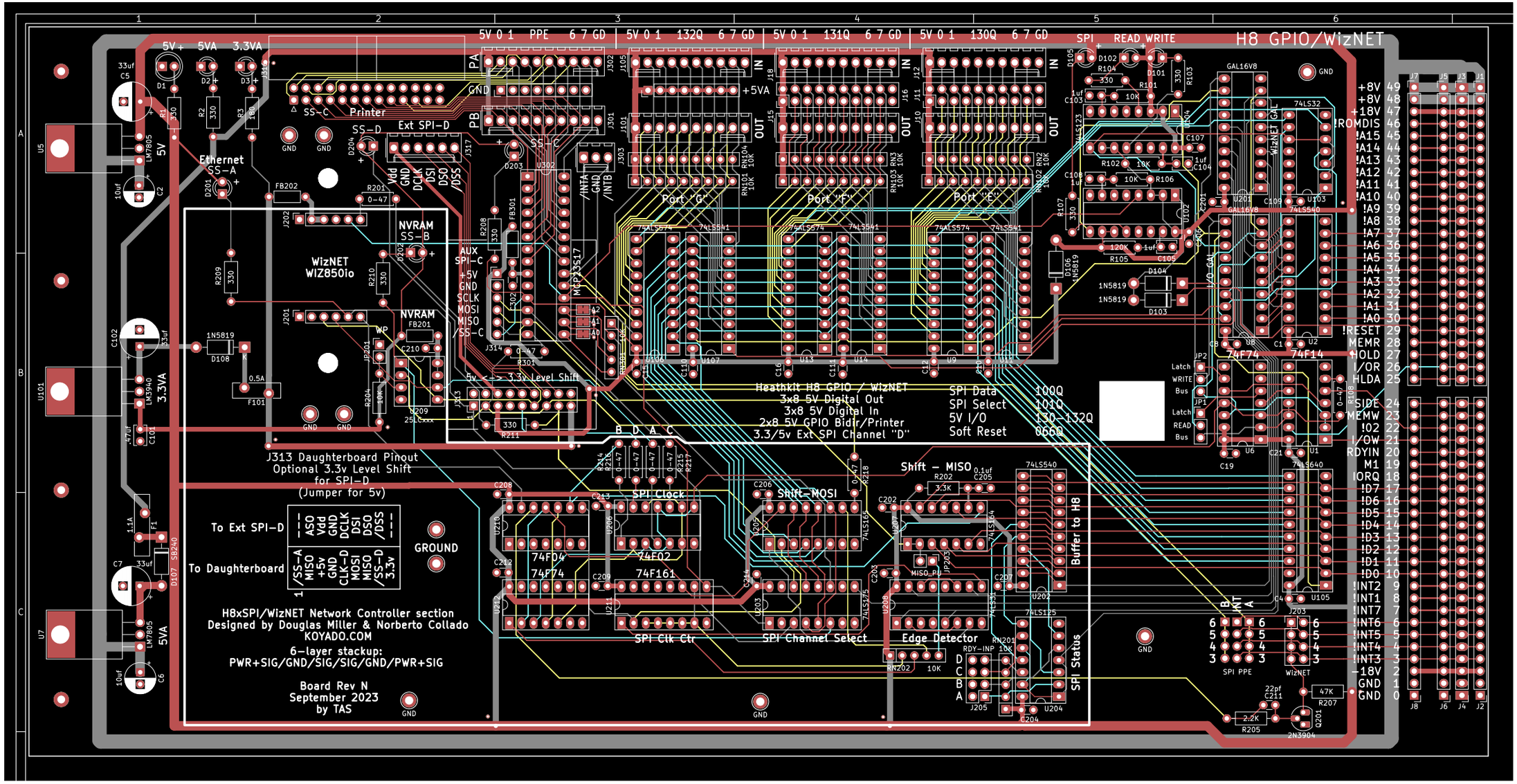
H8 GPIO/WizNET
5V 0 1 PPE 6 7 GD | 5V 0 1 132Q 6 7 GD | 5V 0 1 131Q 6 7 GD | 5V 0 1 130Q 6 7 GD
SPI READ WRITE
5V+ 5VA 3.3VA
Ethernet SS-A
Printer SS-C
Ext SPI-D SS-D
Port "G"
Port "F"
Port "E"
NVRAM SS-B
WizNET WIZ850io
AUX SPI-C
5v <-> 3.3v Level Shift
Heathkit H8 GPIO / WIZNET
3x8 5V Digital Out
3x8 5V Digital In
2x8 5V GPIO Bidir/Printer
3.3/5v Ext SPI Channel "D"
SPI Data
SPI Select
5V I/O
Soft Reset
100Q
101Q
130-132Q
060Q
J313 Daughterboard Pinout
Optional 3.3v Level Shift
for SPI-D
(Jumper for 5v)
To Ext SPI-D
To Daughterboard
GROUND
H8xSPI/WizNET Network Controller section
Designed by Douglas Miller & Norberto Collado
KOYADO.COM
6-layer stackup:
PWR+SIG/GND/SIG/SIG/GND/PWR+SIG
Board Rev N
September 2023
by TAS
SPI Clock
Shift-MOSI
Shift-MISO
SPI Clk Ctr
SPI Channel Select
Edge Detector
Buffer to H8
SPI Status
SPI PPE
WizNET
74F74
74F14
74F04
74F02
74F161
+8V +8V +18V !ROMDIS !A15 !A14 !A13 !A12 !A11 !A10 !A9 !A8 !A7 !A6 !A5 !A4 !A3 !A2 !A1 !A0 !RESET MEMR !HOLD !/OR !HLDA !SIDE !MEMW !02 !/OW RDYIN M1 !IORQ !D7 !D6 !D5 !D4 !D3 !D2 !D1 !D0 !INT2 !INT1 !INT7 !INT6 !INT5 !INT4 !INT3 -18V GND GND