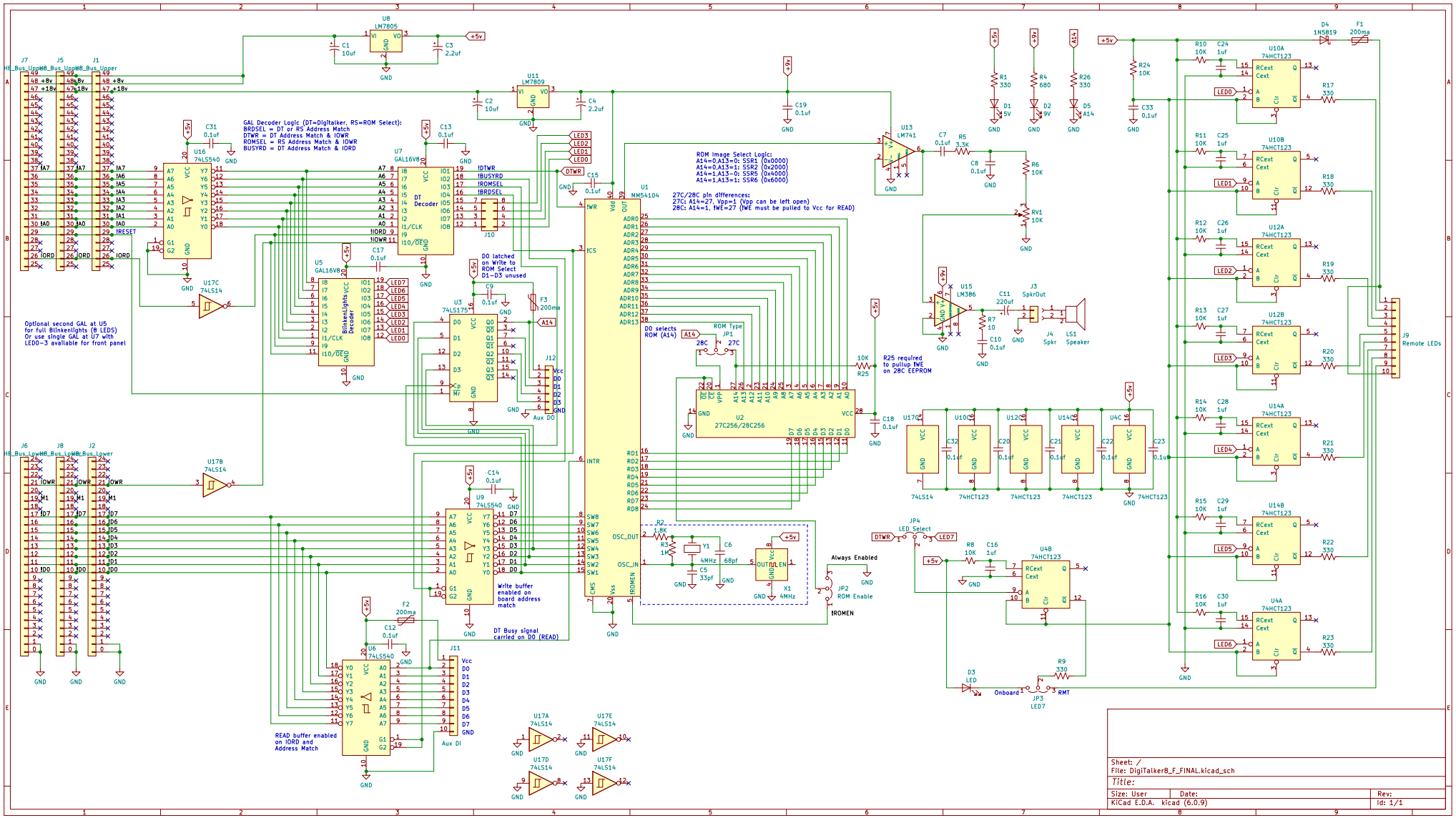
GAL Decoder Logic (DT=DigiTalker, RS=ROM Select):
BRDSEL = DT or RS Address Match
DTWR = DT Address Match & !OWR
ROMSEL = RS Address Match & !OWR
BUSYRD = DT Address Match & !ORD
ROM Image Select Logic:
A14=0,A13=0: SSR1 (0x0000)
A14=0,A13=1: SSR2 (0x2000)
A14=1,A13=0: SSR5 (0x4000)
A14=1,A13=1: SSR6 (0x6000)
27C/28C pin differences:
27C: A14=27, Vpp=1 (Vpp can be left open)
28C: A14=1, !WE=27 (!WE must be pulled to Vcc for READ)
Optional second GAL at U5
for full BlinkenLights (8 LEDS)
Or use single GAL at U7 with
LED0-3 available for front panel
DO latched
on Write to
ROM Select
D1-D3 unused
DO selects
ROM (A14)
ROM Type
R25 required
to pullup !WE
on 28C EEPROM
Always Enabled
!ROMEN
Write buffer
enabled on
board address
match
DT Busy signal
carried on D0 (READ)
READ buffer enabled
on !ORD and
Address Match
Onboard
RMT
Remote LEDs
Aux DO
Aux DI
Speaker
Sheet: /
File: DigiTalker8_F_FINAL.kicad_sch
Title:
Size: User
Date:
Rev:
KiCad E.D.A. kicad (6.0.9)
Id: 1/1